

# DATA SHEET

CHIP RESISTORS RF ATTENUATORS ATV321 (Pb Free) SIZE 0404







## <u>SCOPE</u>

This specification describes ATV321 series chip attenuators with lead-free terminations made by thick film process.

SERIES

AT

#### ORDERING INFORMATION

**Chip Resistors RF Attenuators** 

Part number is identified by the series, size, tolerance, packing type, temperature coefficient, taping reel and resistance value.

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## YAGEO ORDERING CODE

#### CTC CODE

| ATV321 | <u>X</u> | <u>X</u> | <u>X</u> | <u>XX</u> | <u>XXXX</u> | L   |
|--------|----------|----------|----------|-----------|-------------|-----|
|        | (I)      | (2)      | (3)      | (4)       | (5)         | (6) |

#### (I) TOLERANCE

 $B = \pm 0.2 \text{ dB}$  $C = \pm 0.3 \text{ dB}$  $D = \pm 0.5 \text{ dB}$  $F = \pm 1 \text{ dB}$  $G = \pm 2 \text{ dB}$ 

## (2) PACKAGING TYPE

R = Paper/PE taping reel

#### (3) TEMPERATURE COEFFICIENT OF RESISTANCE

- = Not applicable

#### (4) TAPING REEL

- 07 = 7 inch dia. Reel
- 7H = Half quality packing on 7"reel

(non preferrend)

#### (5) ATTENUATION VALUE

I dB to 20 dB shown in the table of "Attenuation value, tolerance v.s. 12 NC code".

#### (6) RESISTOR TERMINATIONS

L = Lead free terminations (pure Tin)

#### Attenuation value, tolerance v.s. 12 NC code

| Attenuation | Standar  | ď                | Optio   | nal              |
|-------------|----------|------------------|---------|------------------|
| value (dB)  | Tol.(dB) | 12 NC            | Tol.(dB | ) 12 NC          |
| I           | ±0.3     | 2350 703 11012 L | ±0.2    | 2350 703 11011 L |
| 2           | ±0.3     | 2350 703 11022 L | ±0.2    | 2350 703 11021 L |
| 3           | ±0.3     | 2350 703 11032 L | ±0.2    | 2350 703 11031 L |
| 4           | ±0.3     | 2350 703 11042 L | ±0.2    | 2350 703 11041 L |
| 5           | ±0.3     | 2350 703 11052 L | ±0.2    | 2350 703 11051 L |
| 6           | ±0.5     | 2350 703 11063 L | ±0.3    | 2350 703 11062 L |
| 7           | ±0.5     | 2350 703 11073 L | ±0.3    | 2350 703 11072 L |
| 8           | ±0.5     | 2350 703 11083 L | ±0.3    | 2350 703 11082 L |
| 9           | ±0.5     | 2350 703 11093 L | ±0.3    | 2350 703 11092 L |
| 10          | ±0.5     | 2350 703 11103 L | ±0.3    | 2350 703 11102 L |
| 15          | ±1.0     | 2350 703 11154 L | ±0.5    | 2350 703 11153 L |
| 20          | ±2.0     | 2350 703 11205 L | ±1.0    | 2350 703 11204 L |

#### **ORDERING EXAMPLE**

The ordering code of an ATV321 attenuator with  $2\pm0.3$  dB attenuation, supplied in 7-inch tape reel is: ATV321CR-072dBL.

#### NOTE

- The "L" at the end of the code is only for ordering. On the reel label, the standard CTC will be mentioned an additional stamp "LFP"= lead free production.
- 2. Products with lead in terminations fulfil the same requirements as mentioned in this datasheet.
- 3. Products with lead in terminations will be phased out in the coming months (before July 1st, 2006)





## MARKING

No marking.

## CONSTRUCTION

The attenuators are constructed on a high-grade ceramic body (aluminium oxide). The internal circuit is applied to the top surface of the substrate, and its design determines the required attenuation value. The attenuation layer is covered with a protective coating and a marking dot indicates input pin I as shown in the connection diagram of Fig. I.

Finally, the four external end terminations are added. To guarantee optimum solderability the outer layer of the terminations are lead free (pure Tin).



#### **DIMENSIONS**

| Table I |            |
|---------|------------|
| TYPE    | ATV321     |
| L (mm)  | 1.0 ±0.10  |
| W (mm)  | 1.0 ±0.10  |
| T (mm)  | 0.35 ±0.05 |
| A (mm)  | 0.33 ±0.10 |
| B (mm)  | 0.15 ±0.10 |
| P (mm)  | 0.65 ±0.10 |
| D (mm)  | 0.25 ±0.10 |
|         |            |





Chip Resistors RF Attenuators AT SERIES V321 (0404) / Pb Free

## ELECTRICAL CHARACTERISTICS

| Table 2  |                |  |
|--|----------------|--|
| CHARACTERISTICS                                  |                | ATV321 / 40 mW   |
| Attenuation Range                                |                | l dB to 20 dB  |
|  | I dB to 5 dB   | ±0.3 dB (optional: ±0.2 dB)                                  |
| Attenuation tolerance                            | 6 dB and 10 dB | $\pm 0.5$ dB (optional: $\pm 0.3$ dB)                        |
|  | I5 dB          | $\pm$ 1.0 dB (optional: $\pm$ 0.5 dB)                        |
|  | 20 dB          | $\pm 2.0 \text{ dB} \text{ (optional: } \pm 1.0 \text{ dB)}$ |
| Characteristic impedance                         |                | 50 Ω   |
| Frequency Range                                  | I              | dB to 10 dB DC to 2.5 GHz                                    |
|  | 15 d           | B and 20 dB DC to 2.0 GHz                                    |
| VSWR   |                | I.3 max.   |
| <b>1aximum permissible voltage</b> 50 V (DC or R |                | 50 V (DC or RMS)   |
| Power rating 40 m                                |                |  |

# FOOTPRINT AND SOLDERING PROFILES

For recommended footprint and soldering profiles, please see the special data sheet "Chip resistors mounting".

## ENVIRONMENTAL DATA

For material declaration information (IMDS-data) of the products, please see the separated info "Environmental data".

## PACKING STYLE AND PACKAGING QUANTITY

**Table 3** Packing style and packaging quantity

| PRODUCT TYPE | PACKING STYLE              | REEL DIMENSION            | QUANTITY PER REEL           |
|--------------|----------------------------|---------------------------|-----------------------------|
| ATV321       | Paper / PE Taping Reel (R) | 7" (178 mm)               | 10,000 units                |
|              |                            | 7" (half quality packing) | 5,000 units / not preferred |

#### NOTE

1. For Paper/PE tape and reel specification/dimensions, please see the special data sheet "Packing" document.

## FUNCTIONAL DESCRIPTION

## **POWER RATING**

ATV321 rated power at 70°C is 40 mW

## **RATED VOLTAGES**

The DC or AC (rms) continuous working voltage corresponding to the rated power is determined by the following formula:

 $V=\sqrt{(P \times R)}$ 

Where

V=Continuous rated DC or AC (rms) working voltage (V)

P=Rated power (W)

R=Resistance value ( $\Omega$ )





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## TESTS AND REQUIREMENTS

Table 4 Test condition, procedure and requirements

| EST                | TEST METHOD   | PROCEDURE                                      | REQUIREMENTS               |
|--------------------|---|--|----------------------------|
| Thermal Shock      | MIL-STD-202F-method 107G;   | At -65 (+0/-10) °C for 2 minutes and at +125   | Max.: ±0.3 dB              |
|                    | IEC 60115-1 4.19  | (+10/–0) °C for 2 minutes; 25 cycles           | No visible damage          |
| Short Time         | MIL-R-55342D-Para 4.7.5;  | 2.5 × RCWV applied for 5 seconds at room       | Max.: ±0.3 dB              |
| Overload           | IEC 60115-1 4.13  | temperature                                    | No visible damage          |
| Insulation         | MIL-STD-202F-method 302;  | RCOV for 1 minute                              | ≥10 GΩ                     |
| Resistance         | IEC 60115-1 4.6.1.1   | Type ATV321                                    |                            |
|                    |   | <b>Voltage (DC)</b> 50 V                       |                            |
| Resistance to      | MIL-STD-202F-method 210C;   | Unmounted chips; 260 $\pm$ 5 °C for 10 $\pm$ 1 | Max.: ±0.1 dB              |
| Soldering<br>Heat  | IEC 60115-1 4.18  | seconds  | No visible damage          |
| Life               | MIL-STD-202F-method 108A;   | At 70±2 °C for 1,000 hours; RCWV applied for   | Max.: ±0.3 dB              |
|                    | IEC 60115-1 4.25.1  | 1.5 hours on and 0.5 hour off                  |                            |
| Solderability      | MIL-STD-202F-method 208A;   | Solder bath at 245±3 °C                        | Well tinned (≥95% covered) |
|                    | IEC 60115-1 4.17  | Dipping time: 2±0.5 seconds                    | No visible damage          |
| Bending            | Bending JIS C 5202.6.14; Resistors mounted on a 90 mm glass epoxy |  | Max.: ±0.3 dB              |
| Strength           | IEC 60115-1 4.15  | resin PCB (FR4)                                | No visible damage          |
|                    |   | Bending: 5 mm                                  |                            |
| Humidity           | JIS C 5202 7.5;   | 1,000 hours; 40±2 °C; 93(+2/–3)% RH            | Max.: ±0.3 dB              |
| (steady state)     | IEC 60115-8 4.24.8  | RCWV applied for 1.5 hours on and 0.5 hour off |                            |
| Leaching           | EIA/IS 4.13B;   | Solder bath at 260±5 °C                        | No visible damage          |
| C C                | IEC 60115-8 4.18  | Dipping time: 30±1 seconds                     |                            |
| Moisture           | MIL-STD-202F-method 106F;   | 42 cycles; total 1,000 hours                   | Max.: ±0.3 dB              |
| Resistance<br>Heat | IEC 60115-1 4.24.2  | Shown as Fig. 4                                | No visible damage          |

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# <u>REVISION HISTORY</u>

| REVISION  | DATE         | CHANGE NOTIFICATION | DESCRIPTION   |
|-----------|--------------|---------------------|---|
| Version 0 | Oct 12, 2004 | -                   | - First issue of this specification for ATV321 series with lead-free terminations |



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